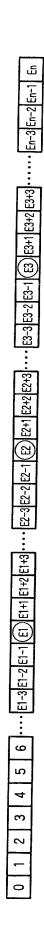
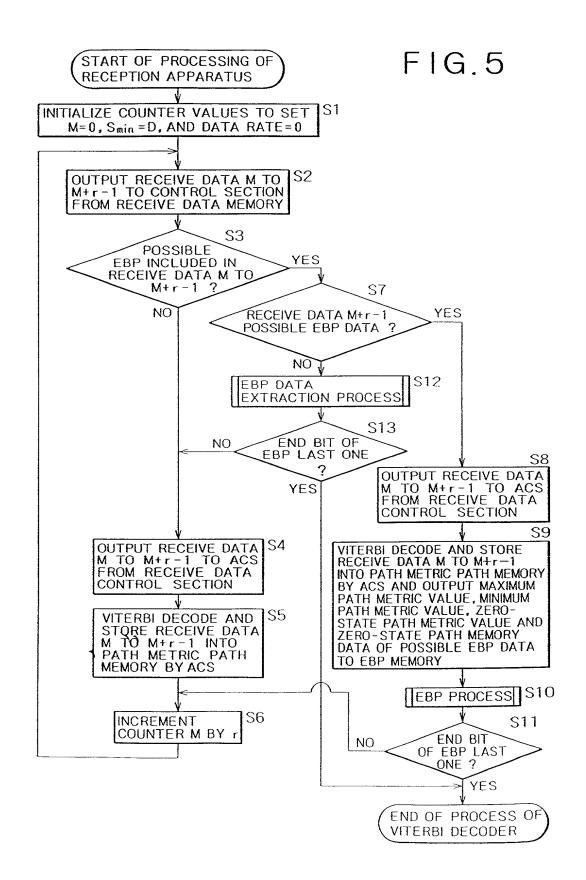


➤ DECODED DATA PATH METRIC PATH MEMORY EBP MEMORY 16 <u>U</u> F1G.3 7 CRC CIRCUIT <u>m</u> CONTROL SECTION RECEIVE DATA MEMORY 12,

F1G.4





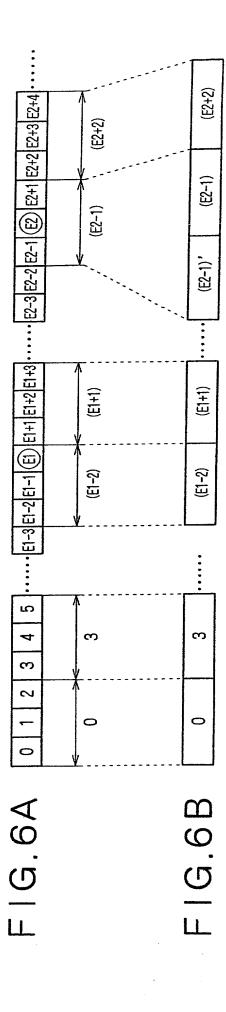


FIG.7

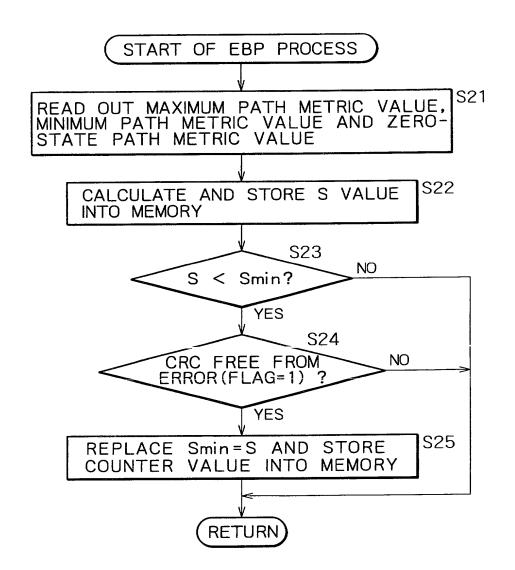


FIG.8

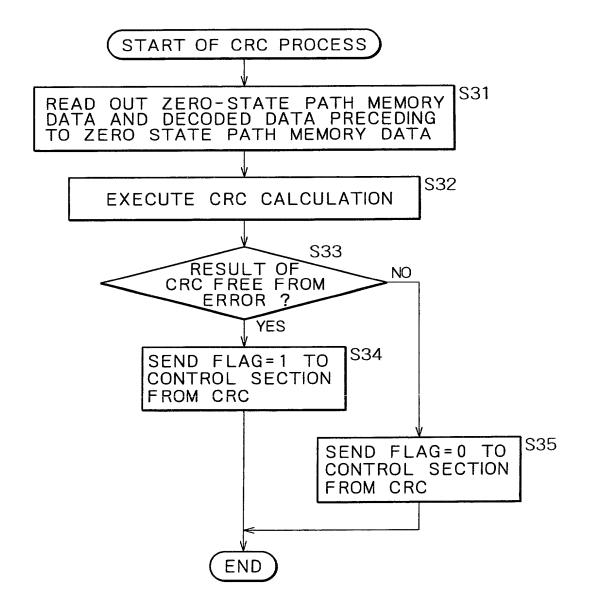


FIG.9

